TinyLSTMs: Efficient Neural Speech Enhancement for Hearing Aids

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Abstract

Modern speech enhancement algorithms achieve remarkable noise suppression by means of large recurrent neural networks (RNNs). However, large RNNs limit practical deployment in hearing aid hardware (HW) form-factors, which are battery powered and run on resource-constrained microcontroller units (MCUs) with limited memory capacity and compute capability. In this work, we use model compression techniques to bridge this gap. We define the constraints imposed on the RNN by the HW and describe a method to satisfy them. Although model compression techniques are an active area of research, we are the first to demonstrate their efficacy for RNN speech enhancement, using pruning and integer quantization of weights/activations. We also demonstrate state update skipping, which reduces the computational load. Finally, we conduct a perceptual evaluation of the compressed models to verify audio quality on human raters. Results show a reduction in model size of roughly 4.4 MB of WM, violating the MS constraint. Moreover, some of the models include many layers of dilated convolutions on the front-end, which require large 64ms frame size and restricted experimental setup, where the training and test data have the same uniform signal-to-noise (SNR), make it difficult to evaluate how the results in [19] would generalize to a low-latency system deployed in an environment with non-stationary SNR.

1. Introduction

The healthy ear is a complex, non-linear system, capable of operating over a large dynamic range. When the ear is damaged, the auditory system can be augmented with a hearing aid (HA), which performs some of the amplification and filtering functions that the ear is no longer able to do. Speech enhancement (SE) could ease listening difficulty in noisy environments, being produced for the listener. The amount of latency that can be tolerated varies depending on the HA type and how the user’s own voice is processed [6,7,8]. Using previous work [6] as a guideline, we target a maximum audio latency of 30ms. For the frame-based approach we employ, with 50% overlap between frames and a causal model, the compute latency constraint for processing each frame is 10ms.

The HA form factor imposes another set of constraints, especially in combination with the frame processing requirement. Due to their small form factor, HAs use a microcontroller unit (MCU) hardware (HW) platform. MCUs enable cheap, low power computation, at the cost of severe memory and computation constraints [9]. The MCU Flash memory restricts the maximum allowable model size (MS), whereas the on-chip SRAM memory upper bounds model working memory (WM), i.e. the memory used to store intermediate results. To achieve efficient computation, the SE model must be quantized to integer data types and we must minimize the number of operations (ops) required per second (ops/s), where an op represents a single addition or multiplication. In this work, we target the STM32F746VE MCU as a typical HW platform, which contains a 216MHz Arm Cortex-M7 [10] with 512KB Flash memory, and 320KB SRAM. We used Mbed OS [11] and CMSIS kernels [12,13]. Table 1 summarizes the SE model constraints.

A few recent works have considered similar constraints. For example, Wilson et al. [5] use a black box optimizer to search for SE models in a family of causal and non-causal models that include compute-heavy convolutions on the model inputs. Model complexity is not explicitly constrained in the search, with the reported models in the 3.7 – 248 MB range, violating the MS constraint. Moreover, some of the models include many layers of dilated convolutions on the front-end, which require large buffers operating at the audio sample rate. This greatly inflates WM and dramatically tightens the constraints on computational time. Hsu et al. [13] investigated separately quantizing the floating point mantissa and exponent values of recurrent and convolutional SE models [13], but these quantized weights would still need to run in floating point HW and incur overheads for decompression. Although, Kim et al. [19] binarize the weights and activations of a particular RNN topology for SE, the practical benefits of binary networks are unclear since current HW and software deployment tools do not support them. Moreover, the large 64ms frame size and restricted experimental setup, where the training and test data have the same uniform signal-to-noise (SNR), make it difficult to evaluate how the results in [19] would generalize to a low-latency system deployed in an environment with non-stationary SNR.
In this work, we present a methodology to generate optimized RNN SE models which meet the requirements in Table 1. Firstly, we demonstrate pruning of SE LSTMs to reduce MS, WM and ops without incurring SE performance degradation. Extending [14], we directly learn the pruning thresholds within the optimization, obviating costly hyperparameter search, resulting in 255x less GPU hours (GPUH) compared to previous work [5]. Secondly, we show for the first time that standard weight and activation quantization techniques extend well to SE RNNs. Moreover, we show that pruning and quantization can be applied to SE RNNs jointly, which is also unique to our work. Finally, we propose a scheme for skipping RNN state updates, reducing the average number of operations.

Our optimized SE models are evaluated using traditional objective metrics, as well as a subjective perceptual evaluation of the audio output. The audio source files we used for this work are available online. A perceptual study is a significant improvement over [4][5][16][18][20][21], because (compressed) SE models exhibit acoustic artifacts not reflected in objective metrics such as SNR. Finally, we profile our models on an MCU to validate that they meet HW constraints, as shown in Table 1.

Table 1: Model constraints. MOps/inf denotes 10^6 operations per frame inference. Target MCU is STM32F746VE.

<table>
<thead>
<tr>
<th>Constraint</th>
<th>Specification</th>
<th>Rationale</th>
</tr>
</thead>
<tbody>
<tr>
<td>Compute Complexity</td>
<td>( \leq 1.55 \text{ MOps/inf} )</td>
<td>Compute budget to achieve ( \leq 10\text{ms} ) latency on target MCU</td>
</tr>
<tr>
<td>Model Size</td>
<td>( \leq 0.5 \text{ MB} )</td>
<td>0.5 MB device Flash memory</td>
</tr>
<tr>
<td>Working Memory</td>
<td>( \leq 320 \text{ KB} )</td>
<td>320 KB device SRAM</td>
</tr>
<tr>
<td>Data Type</td>
<td>Integer</td>
<td>Lowest energy on target MCU</td>
</tr>
<tr>
<td>Perceptual Quality</td>
<td>Minimal degradation vs. uncompressed model</td>
<td>Compression should not affect user preference</td>
</tr>
</tbody>
</table>

2. Background

Let lowercase and uppercase symbols denote vectors and matrices, respectively, and let \( X = [x^1, \ldots, x^N] \in \mathbb{R}^{M \times N} \).

2.1. Speech Enhancement

Let \( x \in \mathbb{R}^N \) denote \( N \) samples of a single channel time-domain speech signal. In SE, \( x \) is corrupted by noise \( v \) and the goal is to extract \( x \) from \( y = x + v \). In this work, denoising is applied in the time-frequency domain, whereby \( y \) is transformed into \( Y \in \mathbb{C}^{B_L \times B_S} \) using a short time Fourier transform where \( B_L \) is the number of frames that \( N \) is decomposed into and \( B_S \) is the number of frequency bins. In this work, the denoiser is a mask \( M \in \mathbb{R}^{B_L \times B_S} \) applied to the spectrogram, such that the approximation of the target is given by \( X = M \odot Y \odot (\mathcal{L}Y) \), where \( \odot \) denotes the Hadamard product and \( \mathcal{L}Y \) is the phase of the noisy input [22]. The mask is a function of \( Y \) and learnable parameters \( \theta \), i.e. \( M = f_{\theta}(Y) \). Specifically, \( f_{\theta}(\cdot) \) is a neural network whose parameters are learned by minimizing a phase-sensitive spectral approximation loss [20]:

\[
L(\theta) = \left\| X^{0.3} - \left| X^{0.3} \right|^2 \right\|_F + 0.113 \left\| X^{0.3} - X^{0.3} \right\|_F^2,
\]

where frames are power-law compressed with an exponent of 0.3 to reduce the dominance of large values.

2.2. Baseline Model Architecture

Due to the latency requirement, we restrict our attention to causal models [3], whereby \( m^n = f_{\theta} \left( \left| y^i, \ldots, y^j \right| \right) \).

2.3.2-3.3 we describe our proposed approach.

The MS is the total number of parameters in all layers, multiplied by the datatype of each matrix. The number of operations required per inference also depends on the number of parameters, since (almost) all of the operations performed in our model are matrix-vector multiplications, which require 2 ops (multiply and add) per parameter. Although the operation count is independent of model quantization, the throughput that can be achieved on real HW is much higher with lower precision integer data types. Therefore, to reduce overall latency, we employ two optimizations: 1) pruning to reduce operations, and 2) weight/activation quantization, which reduces MS and enables deployment with low-precision integer arithmetic [25].

3. Optimizing LSTMs for HA Hardware

This section introduces optimizations for SE models, such as those in Section 2.2 to satisfy the constraints given in Table 1. We begin by delineating the dependence of MS and computational cost on the properties of the model. Then, in Sections 3.2.3 we describe our proposed approach.

We use an architecture consisting of a series of recurrent layers followed by fully connected (FC) layers. The recurrent layers serve to model interactions across time. For a recurrent layer, we use long-short term memory (LSTM) cells, which are stateful and have update rules \( i^t = \sigma(W_{xi}x^t + h^{t-1}W_{hi} + b_i), f^t = \sigma(W_{xf}x^t + h^{t-1}W_{hf} + b_f), o^t = \sigma(W_{xo}x^t + h^{t-1}W_{ho} + b_o), u^t = \tanh(W_{xu}x^t + h^{t-1}W_{hu} + b_u), \)

\[
c^t = i^t \odot c^{t-1} + f^t \odot u^t, \quad h^t = o^t \odot \tanh(c^t),
\]

where \( \sigma \) is the sigmoid function [23]. The baseline architecture consists of 2 unidirectional LSTM layers with 256 units each and 2 FC layers with 128 units each, with batch normalization between the last LSTM and first FC layers. ReLU activation is applied after the first FC layer and sigmoid after the second. In all cases, the spectral input to the network is mapped onto a 128-dimensional mel space [24] and power-law compressed with an exponent of 0.3. The network output, which shares the dimensionality of the input, is inverted using the corresponding transposed mel matrix to produce spectral mask \( M \).

3.1. Structural Pruning

Pruning is a well established network optimization [26][27]. We specifically use structured pruning because it leads to direct benefits in both model size and throughput [28]. This is in contrast to random pruning, which is harder to exploit on real HW, unless the sparsity is very high. We begin by grouping weights in \( \theta \) into a set \( \Gamma \), where \( w_\gamma \in \Gamma \) denotes the set of weights in a particular group, stacked into a vector. The organization of the groups defines the kind of structures we can learn. For FC layers, we group weights by the neuron they are connected to in the previous layer. For LSTM layers, we group weights by the element of \( h^t \) to which they are connected [24]. We assign a binary mask \( r_\gamma = \mathbb{I} \left( |w_\gamma| > r_\gamma \right) \) to each group of weights in layer \( k \), where \( \mathbb{I} \left( \cdot \right) \) denotes the indicator function and \( r_\gamma \geq 0 \) is a learnable threshold. Let \( P = \{r_\gamma, 1 \leq g \leq |\Gamma|\} \) be the set

https://github.com/BoseCorp/efficient-neural-speech-enhancement
The result is that we do not need to perform a hyperparameter
LSTM weights from [14], but we improve upon the manually
for our particular task. We adopt the structural grouping of
unique amongst the pruning literature and is particularly suited
backward pass [29]. The pruning approach described above is

cator function, we approximate it by the sigmoid function in the
ing and $K$
is the number of layers. To differentiate the indicator
function, we approximate it by its sigmoid function in the
backward pass [29]. The pruning approach described above is
unique amongst the pruning literature and is particularly suited.

Let $w \in \mathbb{R}$ denote a real-valued (floating point) value and $Q_{\alpha,\beta}(w)$ its quantized value, where the quantization is performed uniformly over the range $(\alpha, \beta)$ with $2^{\text{bin}} - 1$ levels, i.e. $Q_{\alpha,\beta}(w) = \text{round}((\text{clip}(w, \alpha, \beta) - \alpha)/\zeta) + \alpha$, where $\alpha < \beta$ and $\zeta = (2^{\text{bin}} - 1)(\beta - \alpha)$. For brevity, we omit $\alpha$ and $\beta$ subscripts moving forward. We adopt a standard approach
of making the model resilient to quantized tensors by performing
training-aware quantization [25]. This exposes the model
outputs to quantization noise, while still allowing the model to
backpropagate on real-valued weights. Concretely, (3) becomes

$$\min_{\theta, \{r_{xy}, 1 \leq k \leq K\}} L_Q(Q(\theta \odot P) + \lambda \sum_{g=1}^{\Gamma} r_{gy} ||Q(w_g)||_2)$$

where $\Omega$ is the set of quantization parameters for all weights and activations, $Q(\theta \odot P)$ represents the fact that the masked network weights are quantized, and $L_Q$ denotes that activations are quantized. During backpropagation, the round($\cdot$) operation is ignored [25]. We quantize the weights, activations and model input to 8 bits; the mask itself, is quantized to 16 bits.

3.3. Skip RNN Cells

Finally, we evaluated the skip RNN approach [30], which can be considered a form of dynamic temporal pruning. A binary neuron $g^t \in \{0, 1\}$ is introduced that acts as a state update gate on the candidate LSTM states $s$, representing both $c^t$ and $h^t$ from [2].

$$g^t = \text{round}(\tilde{g}^t), s^t = g^t s^t + (1 - g^t)(\tilde{s}^t - 1)$$

where $\tilde{g}^t$ is the update probability, updated using

$$\Delta \tilde{g}^t = \sigma(W_c e^{-1} + b_c)$$

$$g^{t+1} = g^t \Delta \tilde{g}^t + (1 - g^t)(\tilde{g}^t + \min(\Delta \tilde{g}^t, 1 - \tilde{g}^t))$$

where $c_{-1}$ is the state of the final LSTM layer. Whenever a state update is skipped, the state update probability $g^t$ is increased by $\Delta \tilde{g}^t$ until $g^t$ is high enough that an update occurs, in which case $g^{t+1}$ becomes $\Delta \tilde{g}^t$. Because $g^t$ is fixed when the LSTMs are not updating, (6) only needs to be calculated when the LSTMs are updated.

In practice, this method of skipping updates performs well on training and evaluation metrics, but produced audio artifacts as the mask itself is not updated when the LSTM skips. To remedy this, two exponential moving averages (EMAs) were introduced to smooth the model in time. First, a context vector, $c_{-1} = 0.9 e_{-1} + 0.1 (W_c x^t + b_c)$, is calculated as an EMA projected from the input spectrogram frame, $x_t$, and concatenated to the LSTM output. Second, an EMA is applied to the mask $m$, to calculate a smoothed mask $\tilde{m} = 0.15 m_{t-1} + 0.85 m_{t}$.  

4. Experimental Results

For all experiments, we optimize the objective using stochastic gradient descent in Tensorflow. We use a 32ms frame, 16ms hop size, and 16KHz sample rate for our baseline, pruning, and quantization experiments. For skip RNN experiments, we use frame and hop sizes of 25ms and 6.25ms, respectively. All methods are trained and evaluated with the CHiME2 WSJ0 dataset [31], which contains 7138 training, 2560 development, and 1980 test utterances, respectively. All three subsets include utterances with SNRs in the range -6 to 9dB. Noise data consists of highly non-stationary interference sources recorded in living room settings including vacuum cleaners, television and children. While the dataset is provided in binaural stereo, we pre-process by summing over the channel dimension to obtain a monaural input and target, in contrast to [3], which uses the full binaural input to predict a binaural mask. For final objective evaluations, we use the signal-to-distortion ratio (SDR) [32]. During training, however, we use the simpler scale-invariant signal-to-distortion ratio (SISDR) since it is computationally less expensive and correlates well to SDR [33].
4.1. Baseline Model

We begin by confirming that our baseline SE model is competitive with the state of the art. Our baseline achieves 12.77dB SDR on the CHiME2 development set (Table 2) and 13.70dB SDR on the test set (Table 3), which is comparable to \cite{15,20}.

4.2. Structured Pruning and Quantization

Next, we examine the effect of structural pruning and quantization on the baseline model. In all cases, we set $\lambda = 10^{-5}$. The trade-off between model size and performance is illustrated in Fig. 1 where each point represents a snapshot during the optimization process. We plot the pareto frontier of SISDR values with respect to the MS. Our experiments show that structural pruning can achieve a 47% pruned model with identical performance to the baseline. Moreover, with both pruning and quantization in effect, a 37% pruned model achieves around 0.2dB reduction in SISDR (Pruned (INT8) 1), and a 66% pruned model shows around 0.5dB decay (Pruned (INT8) 2). Table 3 shows SDR evaluations of our models on the CHiME2 test set.

Our optimized models achieve latency suitable for a smaller frame processing time (hop size) in the audio pipeline. However, a small hop size leads to increased inference frequency and energy consumption. So, to address this challenge, we apply the skip RNN architecture on top of our compressed model. Results for “Pruned Skip RNN (INT8)” show 12.07dB SDR on the CHiME2 development set (Table 2) and 12.96dB SDR on the test set (Table 3). Although the skip RNN requires more inferences per second, the skip rate of 63% results in reduced average energy consumption per inference compared to Pruned (INT8) 2.

Finally, Table 3 details each of the models described. Although the models in \cite{20,5} achieve slightly better SISDR/SDR performance, their MS, WM, and MOPs/inf severely violate HA HW constraints. In contrast, Pruned (INT8) model 2 and Pruned Skip RNN (INT8) can be deployed on a real HA MCU and deliver significant SE capabilities. In contrast to \cite{20,5}, our models achieve compute latency in the 2.39-6.71ms range, satisfying the 10ms requirement. Moreover, the proposed models consume significantly less energy per inference than \cite{20,5}, leading to better HA battery life.

4.3. Perceptual Evaluation

Human perception of audio quality is highly subjective and does not always correlate with objective metrics. Therefore, to understand real-world performance, we conducted perceptual studies to get subjective feedback on the quality of the optimized model compared to our baseline. We conducted surveys for both Pruned (INT8) models (Table 2), each consisting of disjoint sets of 50 participants. Two samples were randomly chosen from each of the 6 SNR levels of the CHiME2 evaluation set, for a total of 12 sample utterances. Each participant was randomly presented with paired comparisons of the original and processed utterance for both the baseline and pruned & quantized models, resulting in 24 paired comparisons per participant. Given the prompt Considering both clarity and quality of speech, which recording do you prefer?, the participants rated comparative preference on a 7-point Likert scale \cite{35} ranging from “Strongly prefer unprocessed” to “Strongly prefer enhanced”, with “No preference” as a midpoint.

The results in Fig. 2 show that participants expressed a “moderate preference” on average for the enhanced audio. We note this compares favorably with industry-standard approaches to improving HA speech-in-noise performance, where participants in a similar study expressed a “slight preference” for directionally processed audio compared to unprocessed \cite{35}. We compare preference for the unprocessed (baseline) and the compressed (pruned and quantized) models to the original unprocessed utterances using a Wilcoxon signed rank test \cite{37} and find no statistical difference between ratings across SNRs (Survey 1: $Z = 0.09$, $p = 0.92$; Survey 2: $Z = 0.19$, $p = 0.85$), indicating that participants prefer the enhanced audio, independently of whether it was produced by the baseline or optimized model.

5. Conclusions

Neural speech enhancement is a key technology for future HA products. However, the latency and power consumption constraints for tiny battery-powered HW is extremely hard to meet due to the large NNs required to achieve satisfactory audio performance. In this work, we applied structural pruning and integer quantization of inputs, weights, and activations to reduce model size by 11.9×, compared to the baseline. We also applied skip RNN techniques to further reduce the ops per inference by 1.78× compared to our smallest compressed model. Our optimized models demonstrate negligible degradation in objective (SISDR) metrics and no statistical difference in subjective human perceptual evaluation. While our baseline model has a computational latency of 12.52ms on our target HW platform, the optimized implementation achieves 4.26ms, which is more than sufficient to meet the 10ms compute latency target.
6. References


[12] ARM-software/CMSIS


